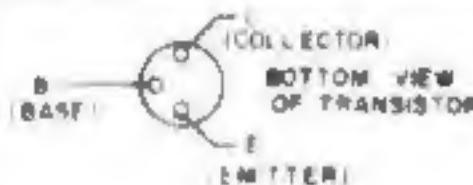


NOTE: The following table summarizes the results of the study.



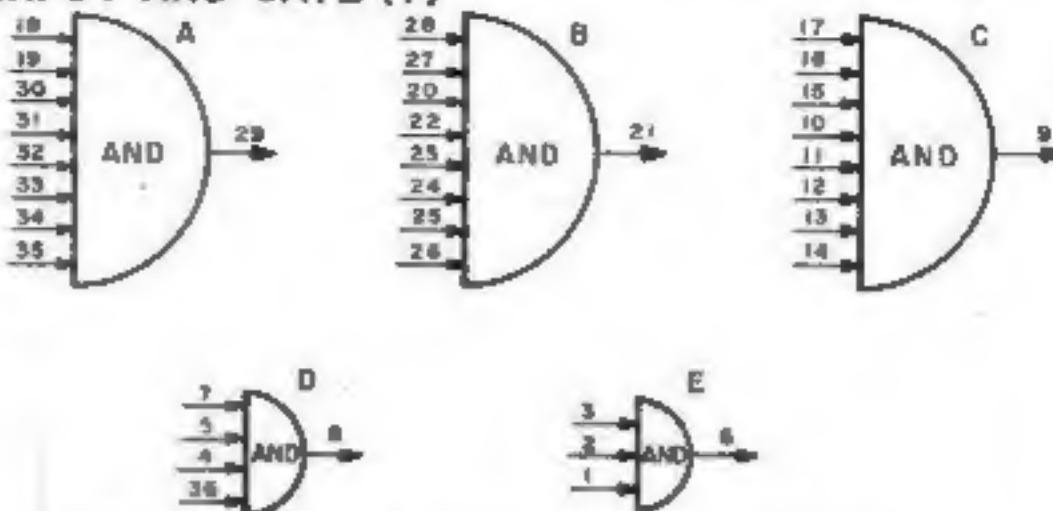
FIVE "AND" GATES ARE PROVIDED. THREE HAVE 6 INPUTS, ONE HAS 4 INPUTS, AND ONE HAS THREE INPUTS. ALL GATES FUNCTION IDENTICALLY. A -6 VOLT LEVEL ON ANY INPUT RESULTS IN A +6 VOLT OUTPUT FROM THE CORRESPONDING GATE. WHEN ALL INPUTS TO A PARTICULAR GATE ARE AT 0 VOLTS, THE CORRESPONDING OUTPUT IS 0 VOLTS. AN EXTERNAL BIAS RESISTOR TO +6 VOLTS IS REQUIRED. WHEN USED AS A "FAN-OUT" GATE, THE OUTPUT OF THE "AND" GATE BECOMES THE INPUT OF THE "FAN-OUT" GATE AND THE INPUT OF THE "AND" GATE BECOMES THE OUTPUT OF THE "FAN-OUT" GATE. WHEN INPUT OF THE "FAN-OUT" GATE IS 0 VOLTS ALL OUTPUTS ARE 0 VOLTS. WHEN THE INPUT IS -6 VOLTS ALL OUTPUTS ARE ISOLATED FROM EACH OTHER.

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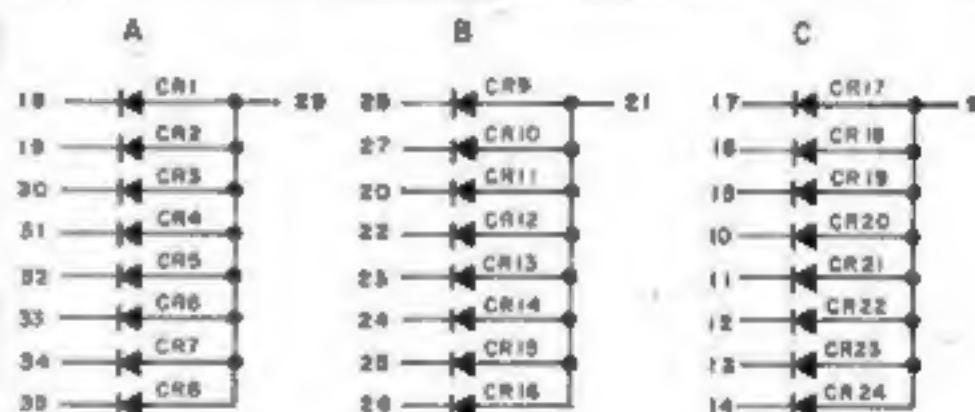
8 INPUT AND GATE (3)
4 INPUT AND GATE (1)
3 INPUT AND GATE (1)

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SYMBOL



NOTE: WHEN USED AS "FAN-OUT" GATE "AND" IS REPLACED BY "F/O" IN SYMBOL.
WHEN TWO OR MORE GATES ARE TIED TOGETHER ONE SYMBOL CAN BE USED
SHOWING PROPER TERMINAL DESIGNATIONS.



ALL CIDDLES ARE D2

C.W. Wright